

Figure 1

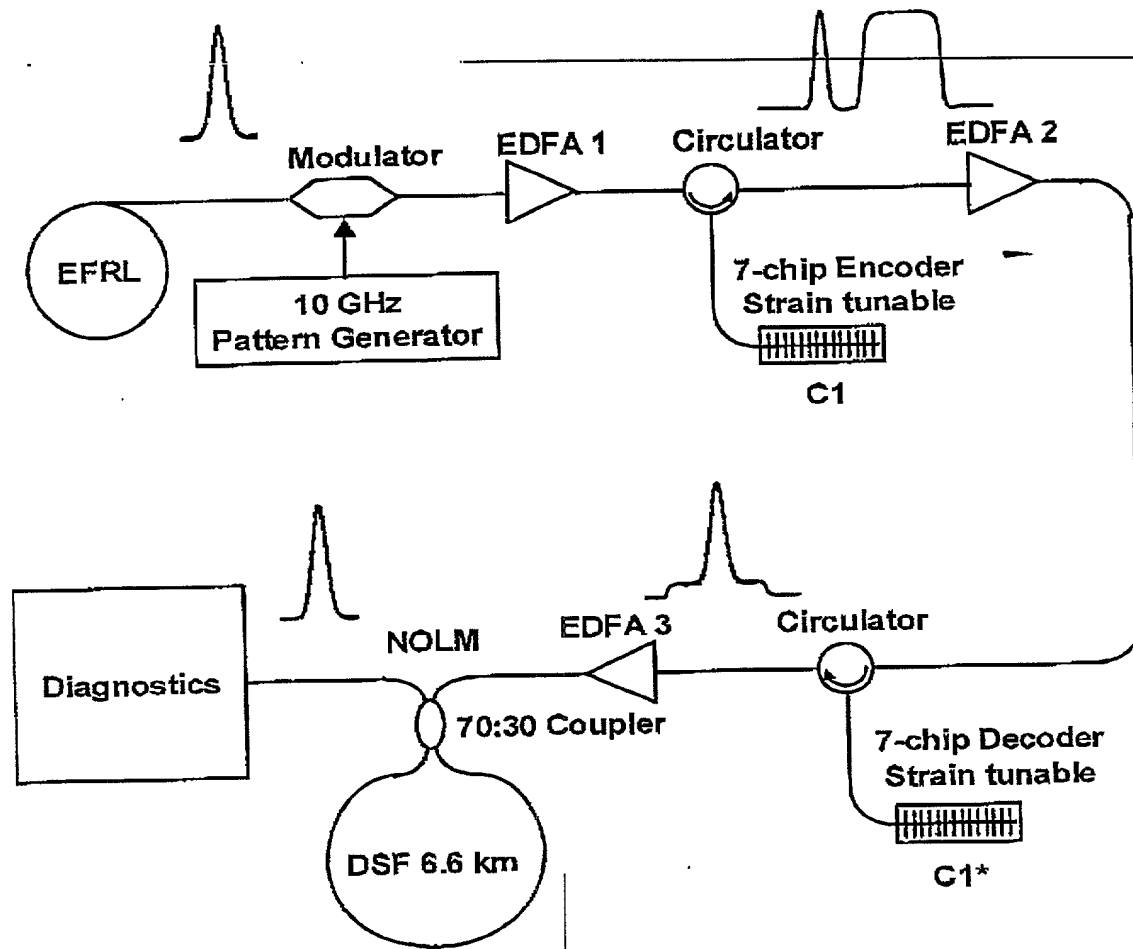


Figure 2

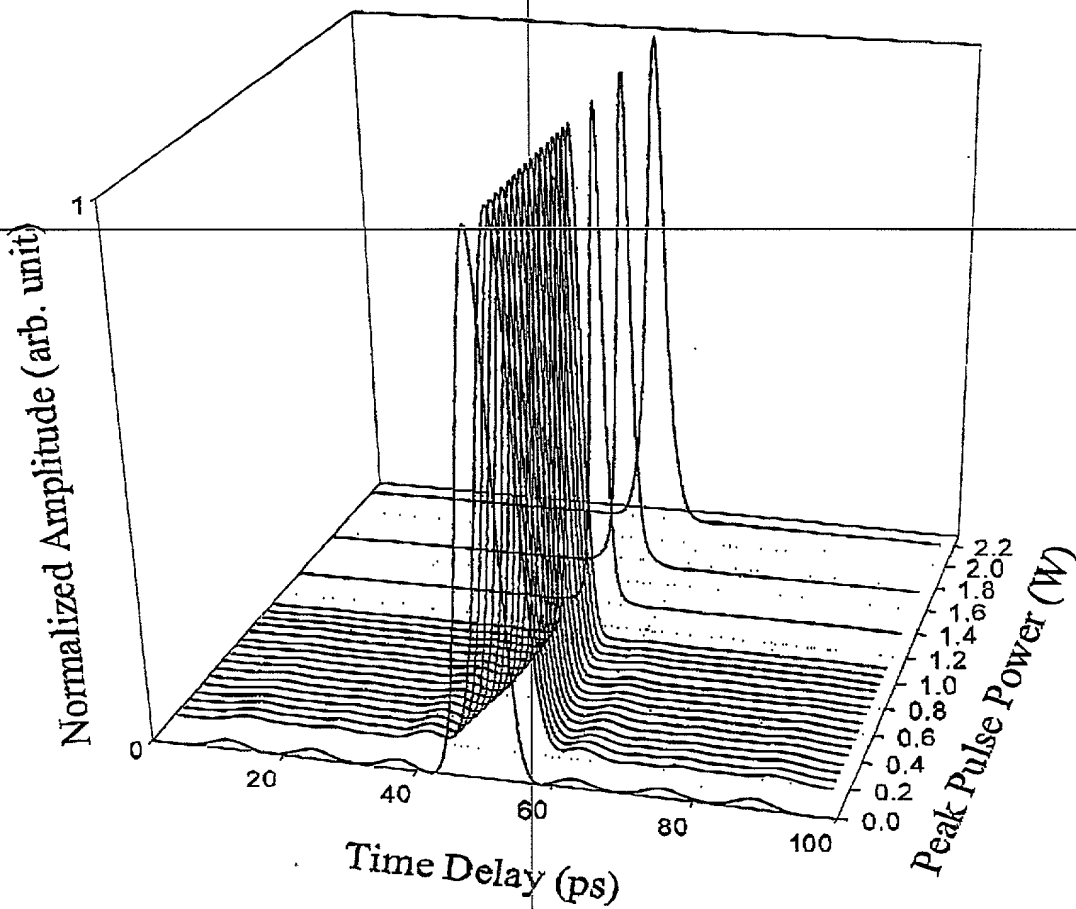


Figure 3(a)

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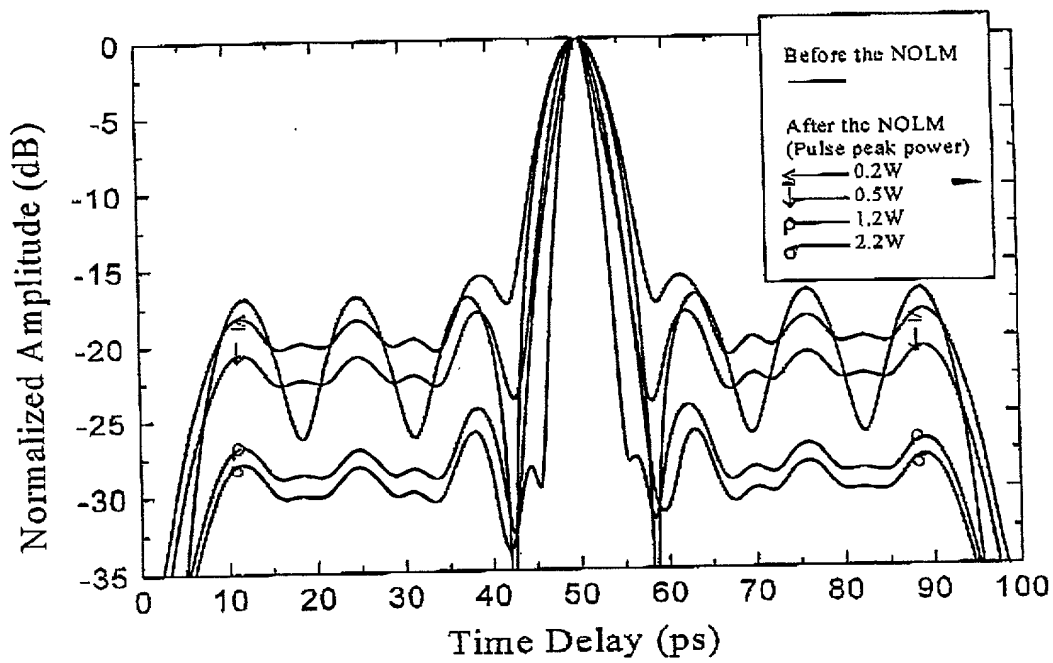


Figure 3(b)

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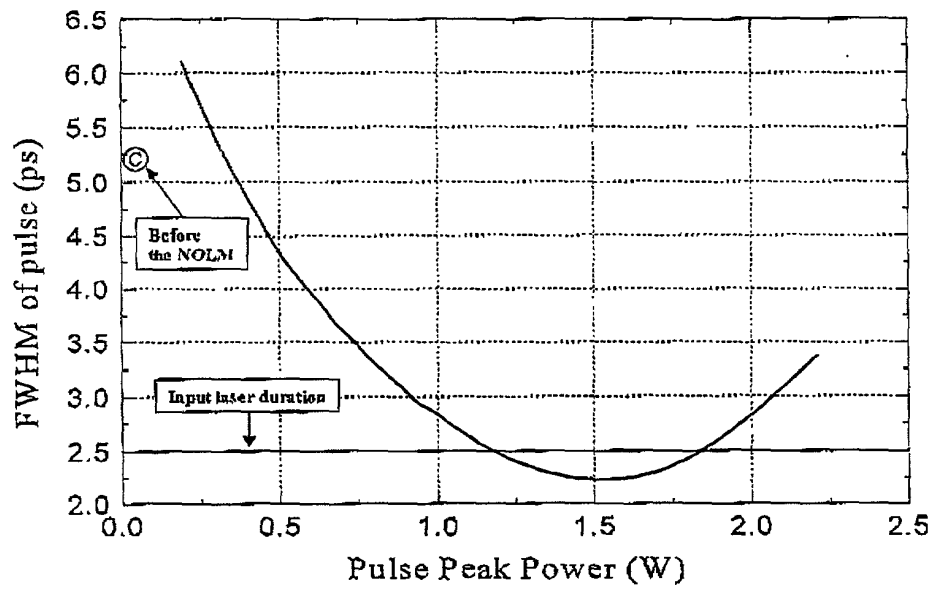


Figure 4(a)

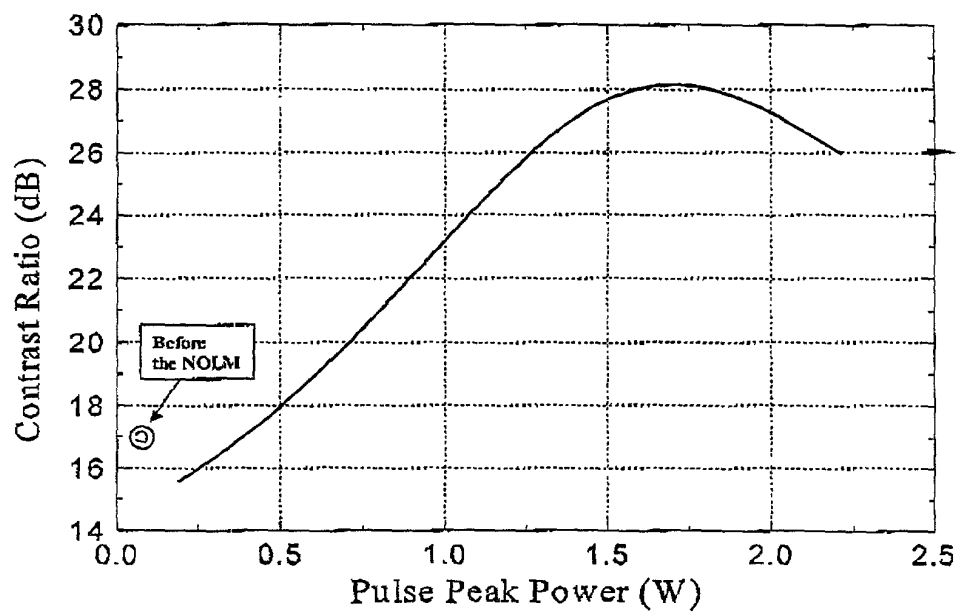


Figure 4(b)

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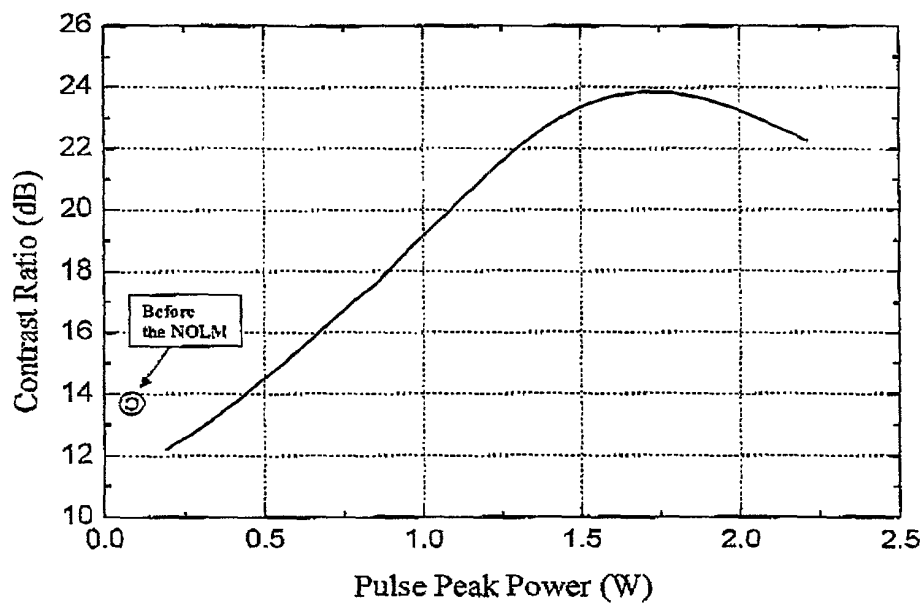


Figure 4(c)

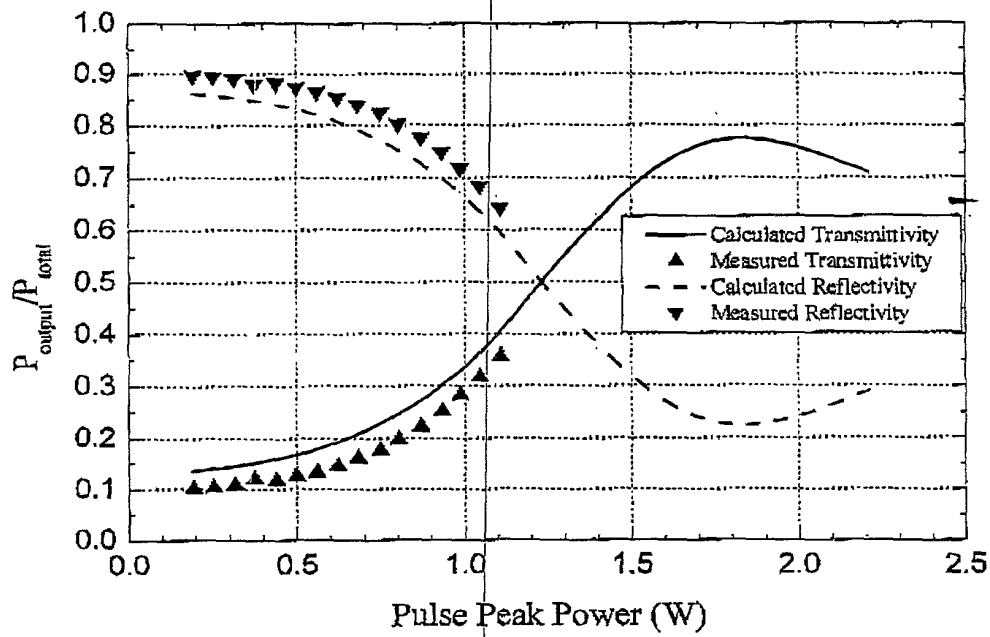


Figure 5

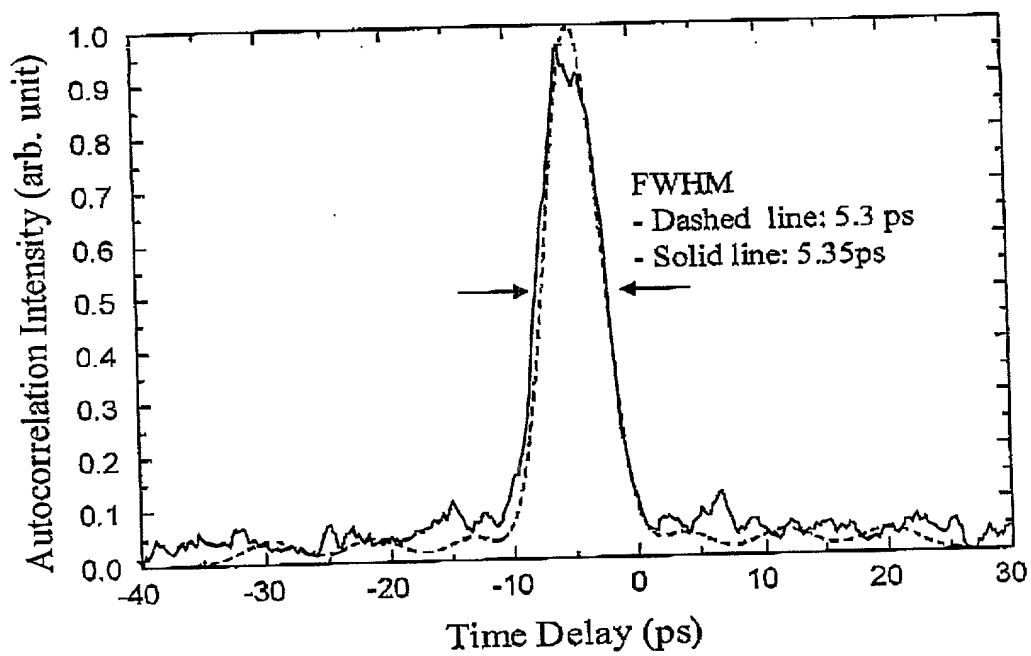


Figure 6(a)

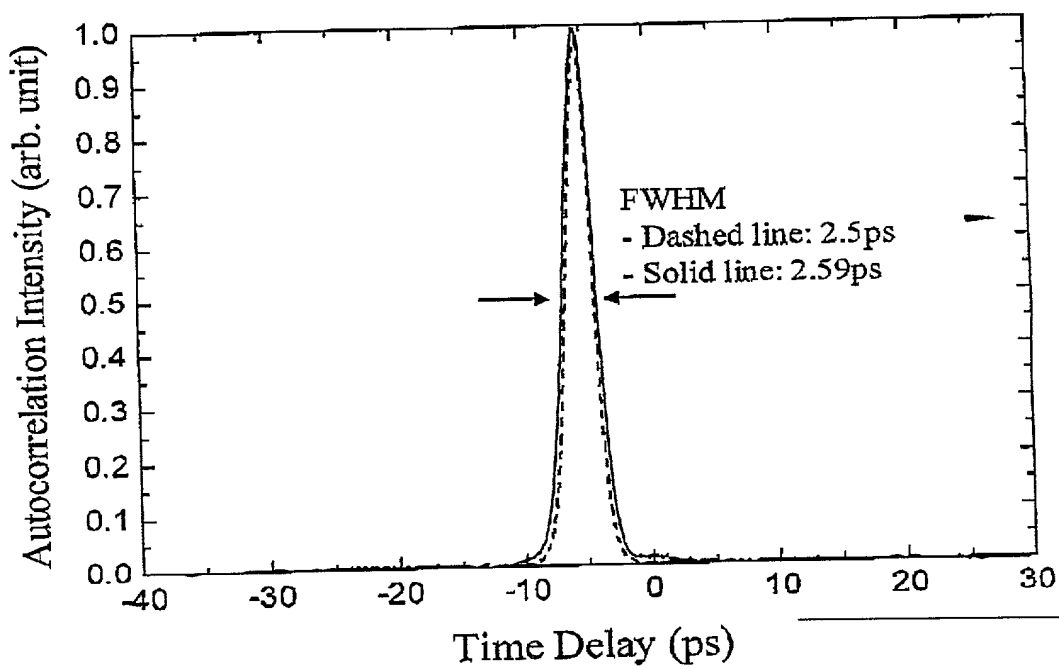


Figure 6(b)

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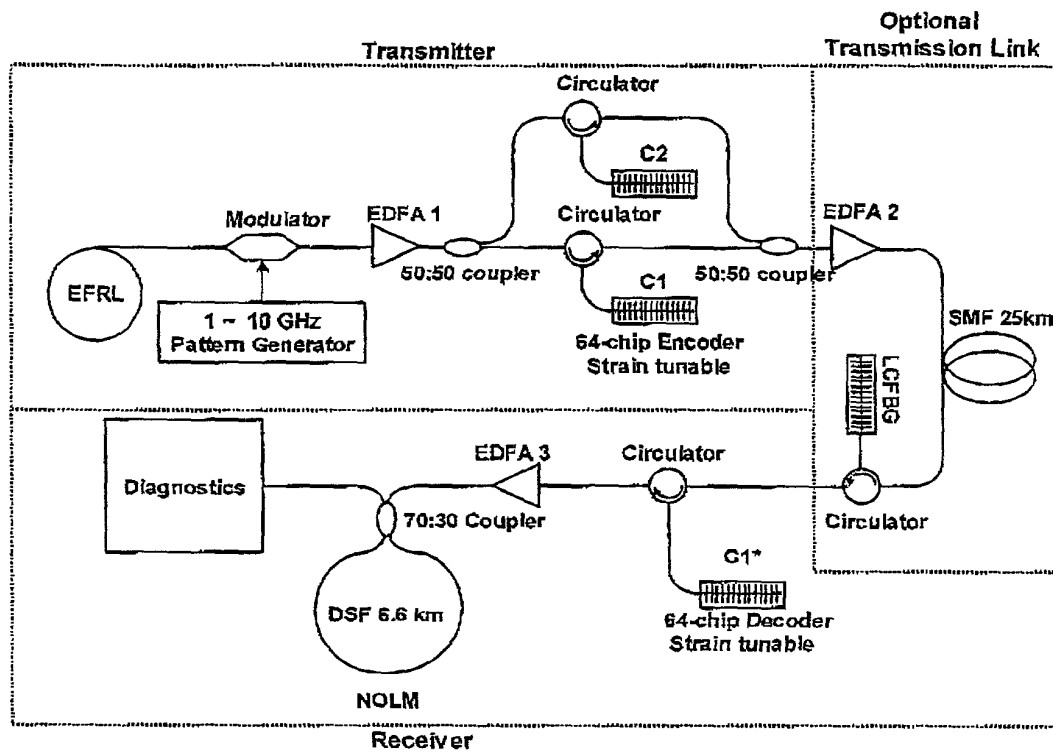


Figure 7(a)

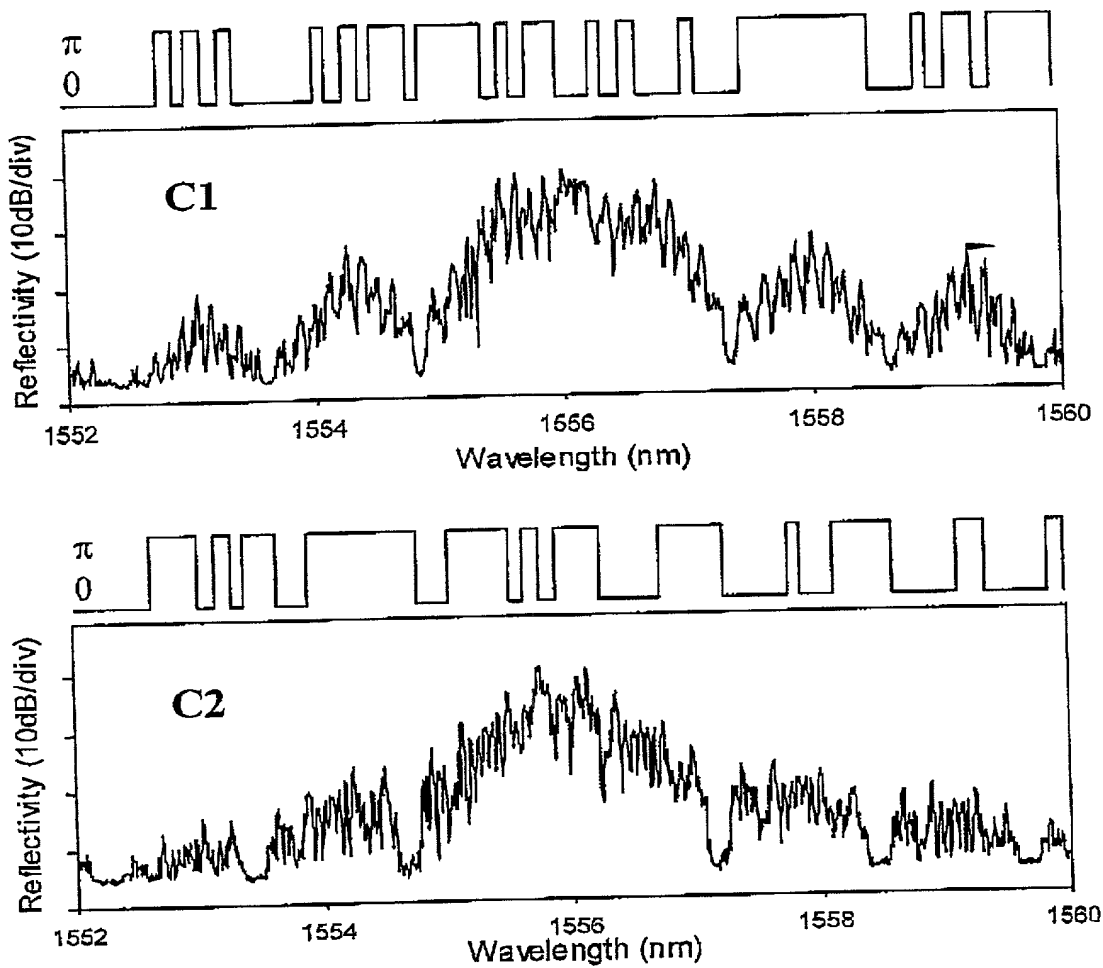


Figure 7(b)

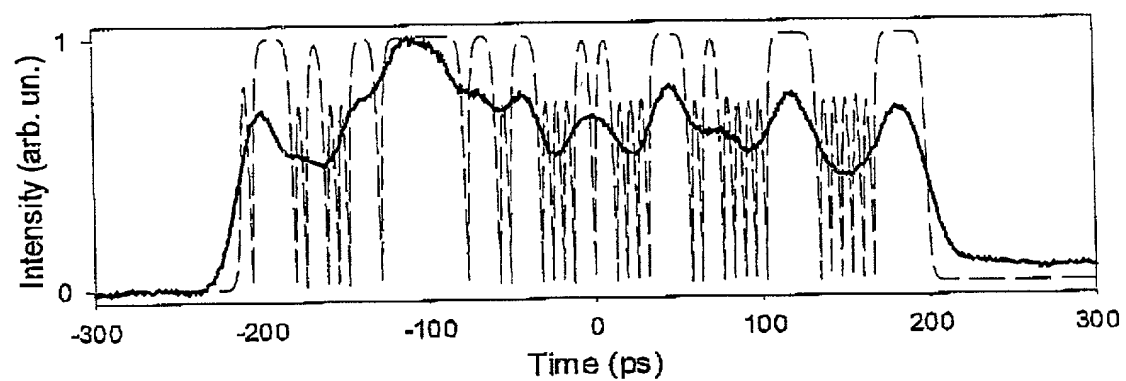
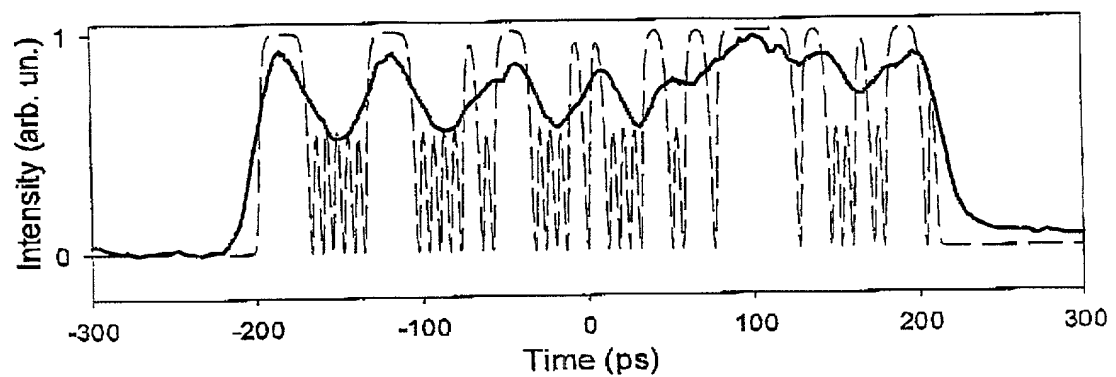


Figure 8

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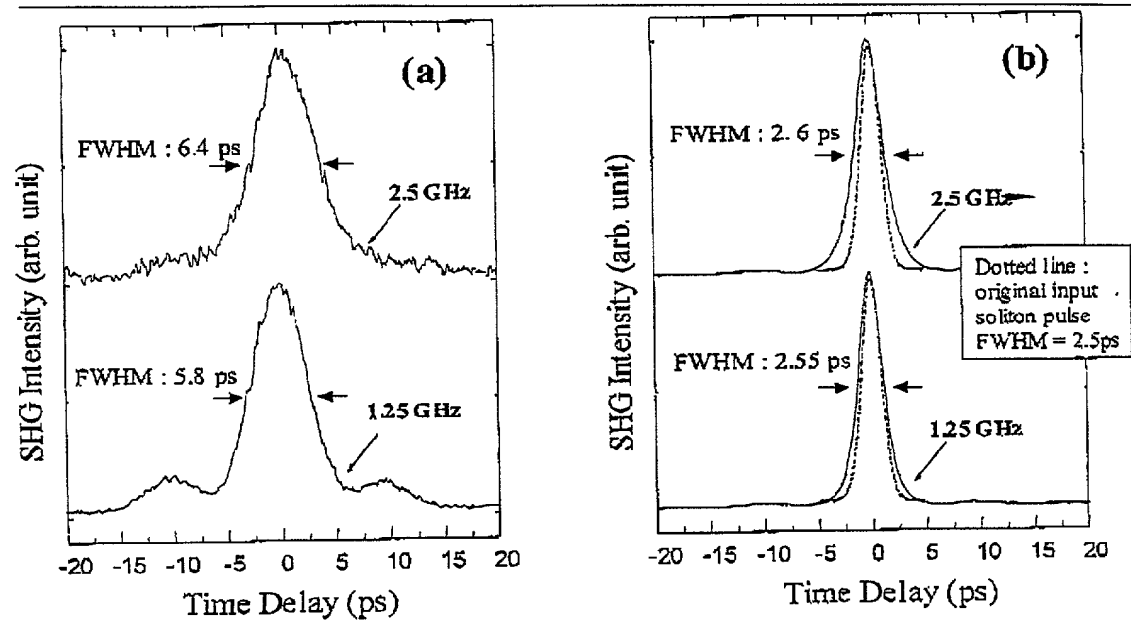


Figure 9



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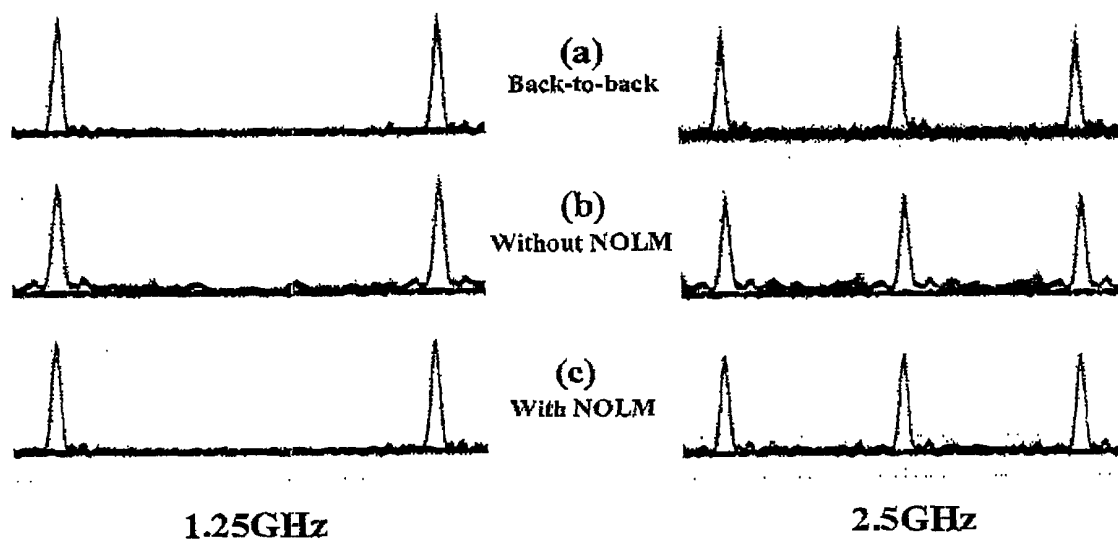


Figure 10

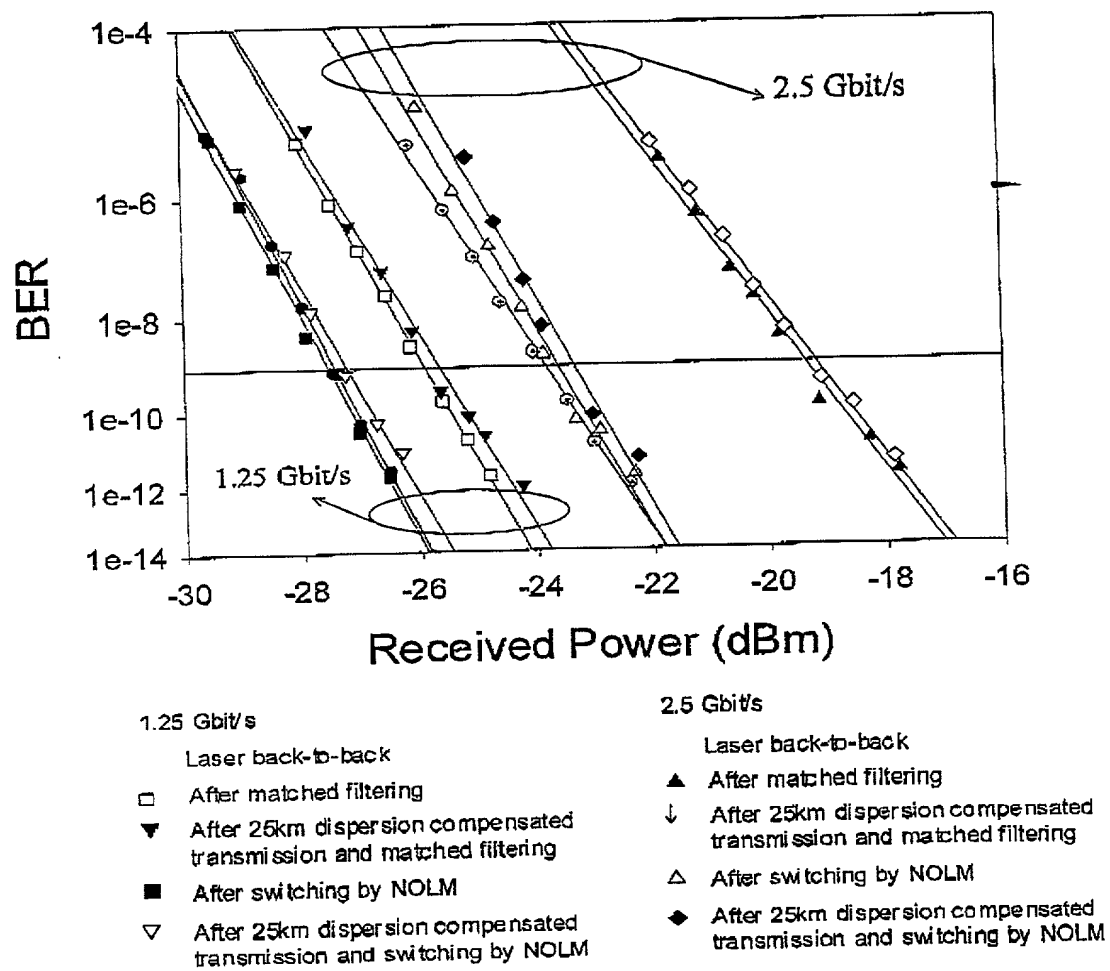


Figure 11

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Back-to-back



Without NOLM



With NOLM



Figure 12(a)

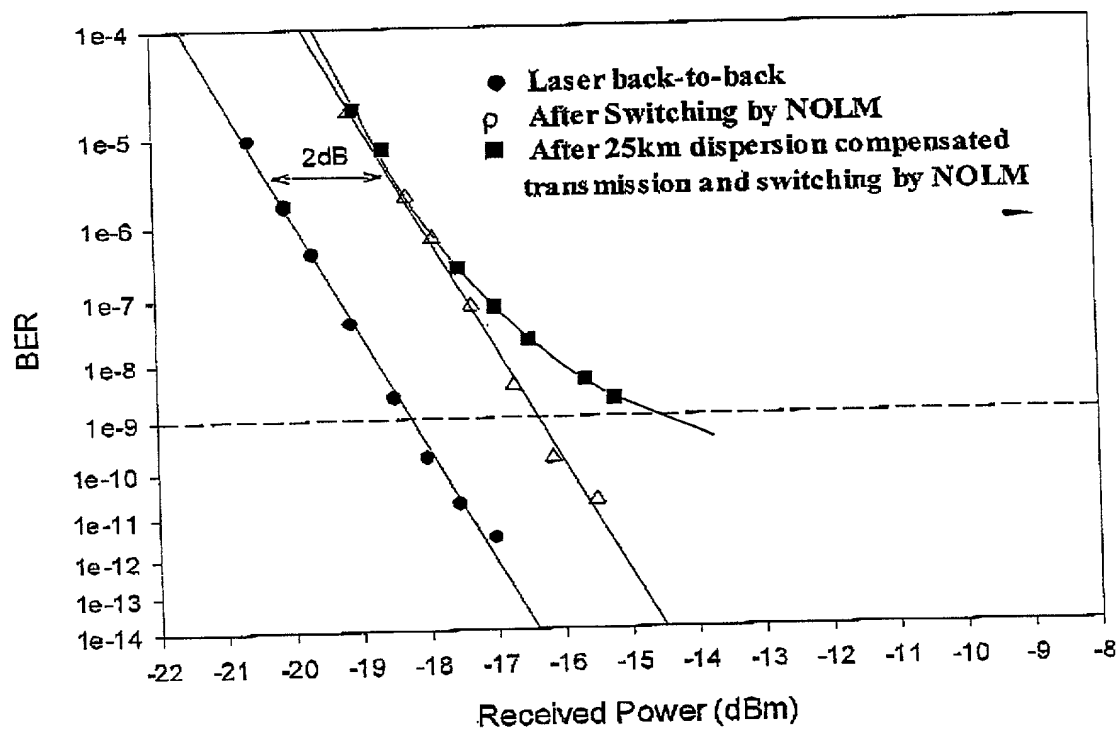
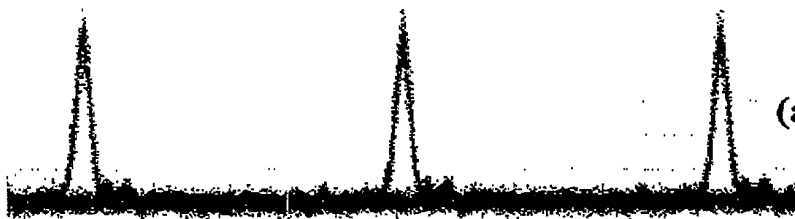
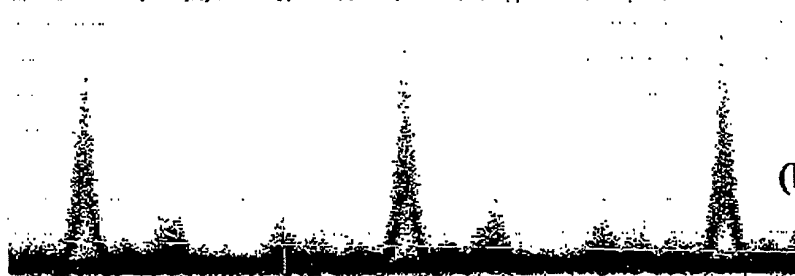


Figure 12(b)



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**(a) Back-to-back****(b) Without NOLM****(c) With NOLM****Figure 13**

#

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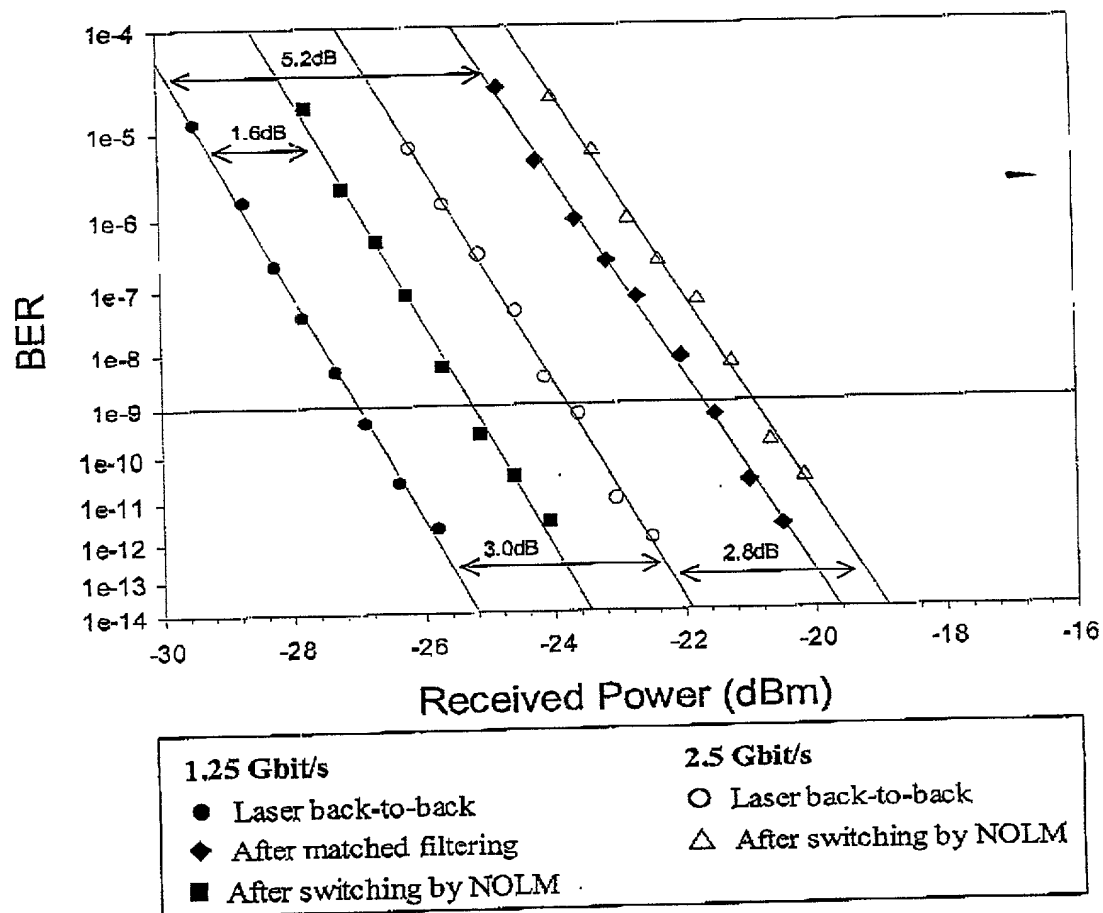


Figure 13

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FIG. 15 is a schematic diagram of a power supply circuit for a motor.

Fig. 15

